Tuesday Evening Panel Discussion:

Tuesday March 22, 2022, 6-8PM

Quantifiable Assurance: From IPs to Platforms

Moderator: Mark Tehranipoor
Glenn Berger **Navy**
Brian Dupaix **AFRL**
Matt Areno **Intel**
George Zafiropooulos **National Instruments**
Ezra Hall **GlobalFoundries**

Hardware vulnerabilities are generally considered more difficult to address than software counterparts because of their persistent nature after fabrication. Thus, it is crucial to assess the security and fix the potential vulnerabilities in the earlier design phases. The focus of the existing security assessment techniques is mainly twofold. We need methodologies to assess the platform-level security by considering both the IP-level security and the impact of the additional parameters introduced during the transition from IP to the platform. This panel discusses the following fundamental questions: What type of additional parameters are introduced during the platform integration? How to define and characterize the impact of these parameters on security? How do the mitigation techniques of one threat impact others? How to estimate assurance quickly and accurately? How to measure and mitigate zero day? How to utilize the significant amount of data collected during design, fabrication, and test for assurance?

Thursday MPW Panel Discussion:

Thursday Afternoon March 24, 2022, 3:30-5:10PM
T&AM MPW Shuttle Program
Sponsor: Dr. Matthew Gadlage, OUSD(R&E) – T&AM Program
Moderators: Mike Netzer, Saverio Fazzari

The Trusted and Assured Microelectronics (T&AM) Program within OUSD Research & Engineering (R&E) aims to provide the U.S. warfighter with the State-of-the-Art (SOTA), assured microelectronics required to meet DoD system modernization goals. One of the primary objectives of the program is to enable access to commercial industry to develop and demonstrate SOTA designs that advance DoD initiatives. T&AM sponsors Multi-Project Wafer (MPW) run opportunities to enable access to SOTA US commercial foundries ≤ 22nm in support of the DoD microelectronics goals and to aid in developing DoD specific PDK's and IP. Currently, T&AM sponsors MPW opportunities with Global Foundries and Intel Foundry Services. The

program is available to relevant designs from the defense industrial base (DIB), gov’t labs, and academia.